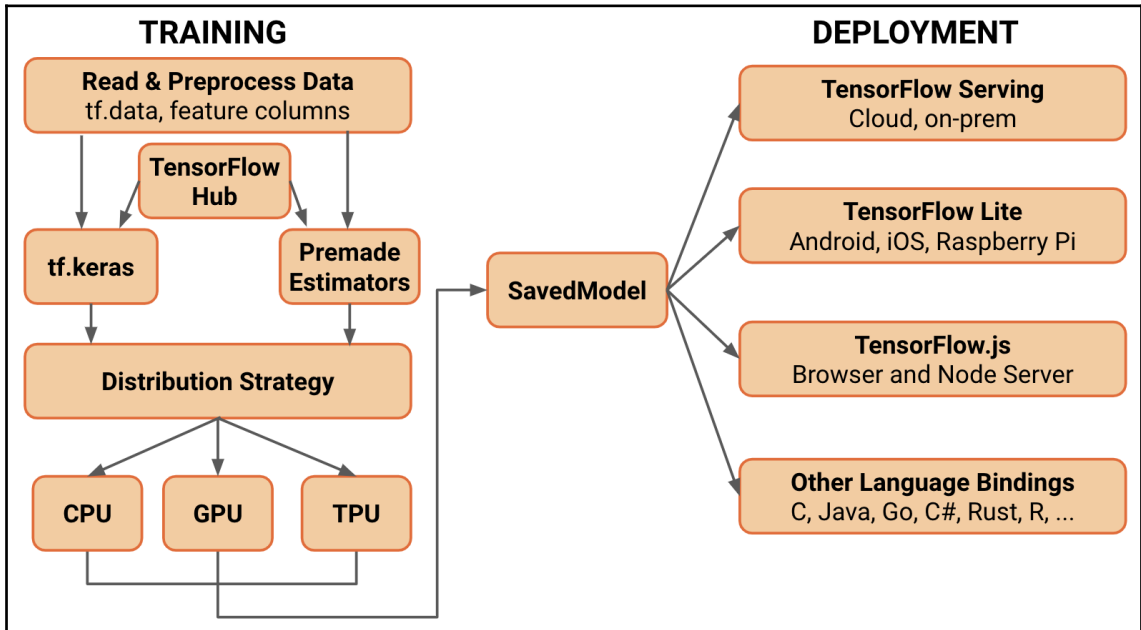
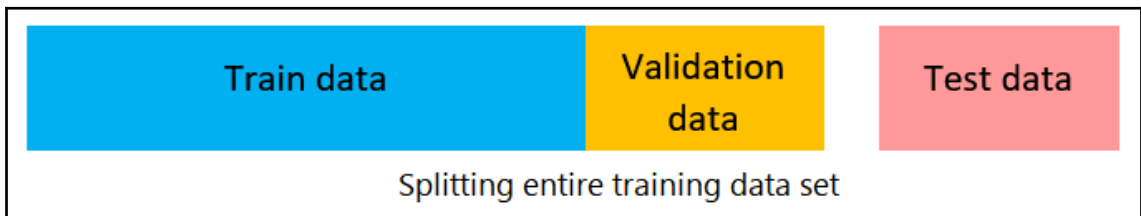
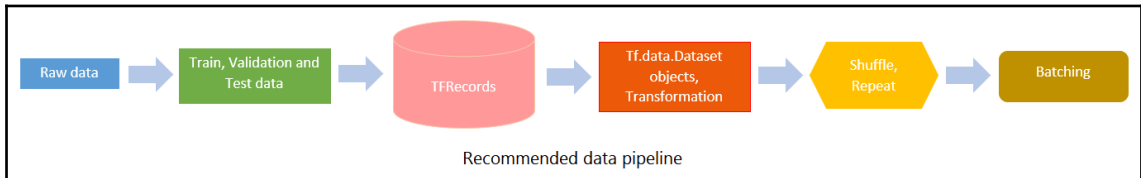
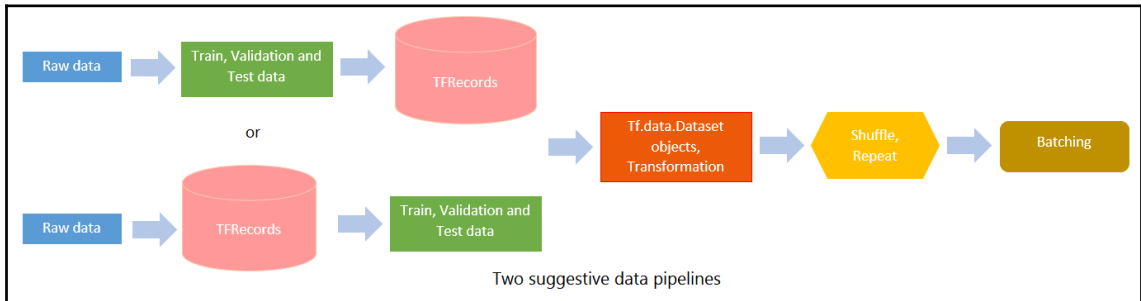
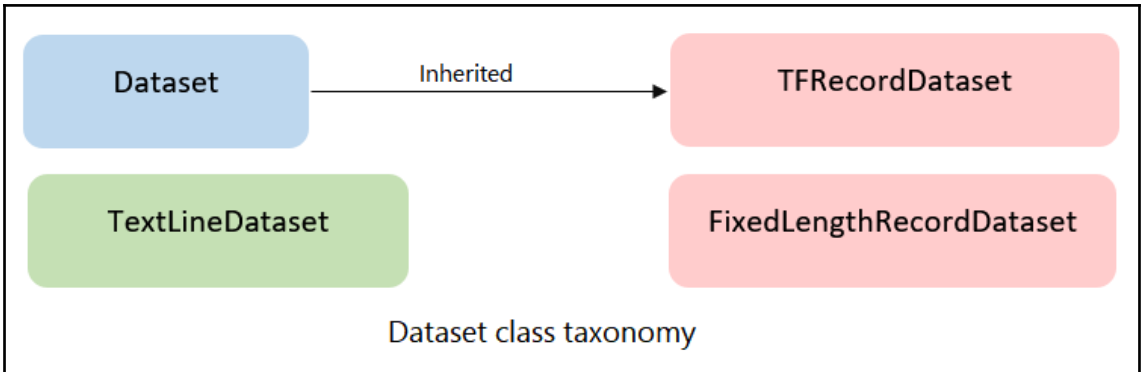
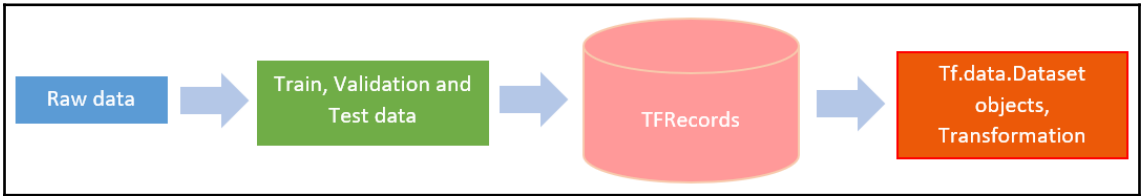
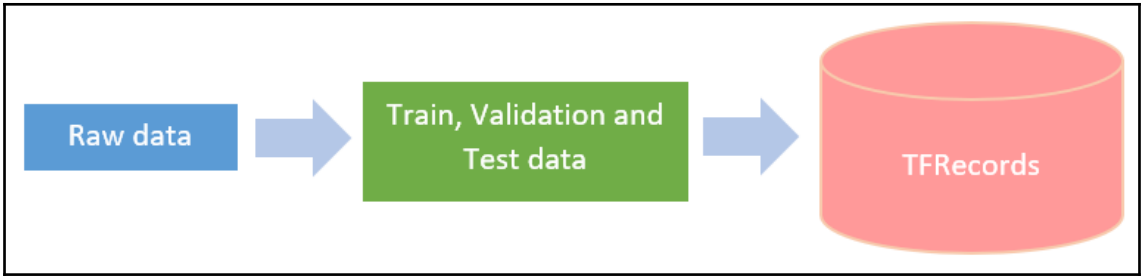


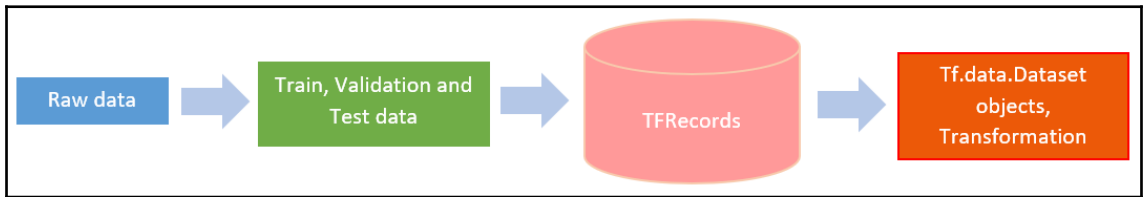
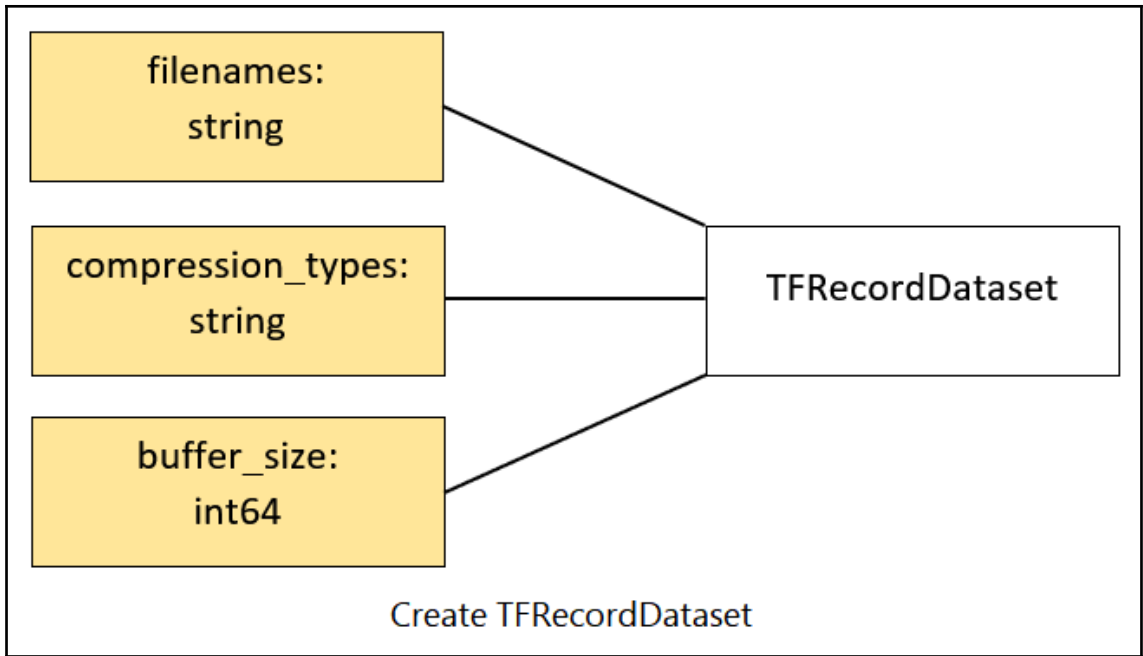
Chapter 1: Getting Started with TensorFlow 2.0

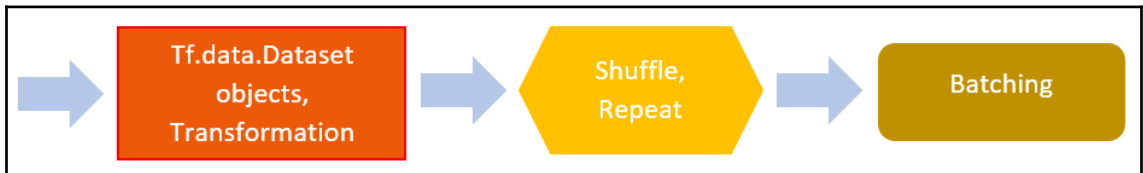
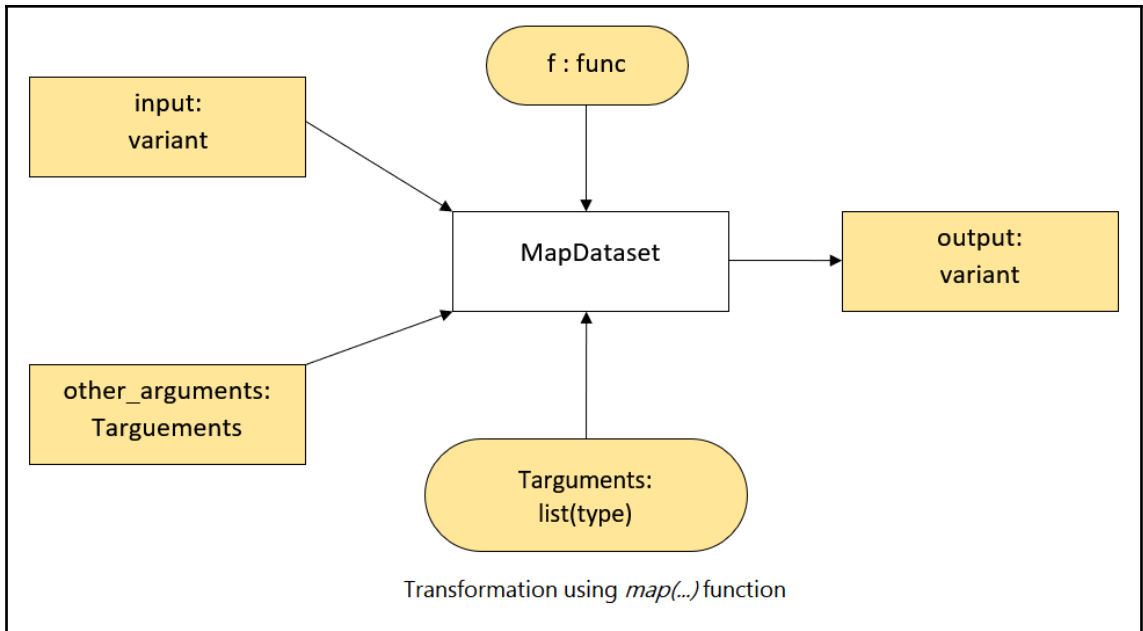


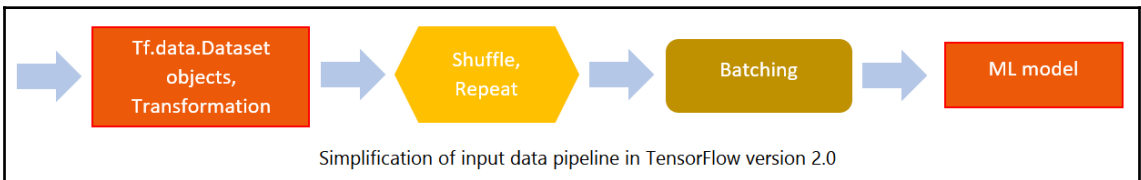
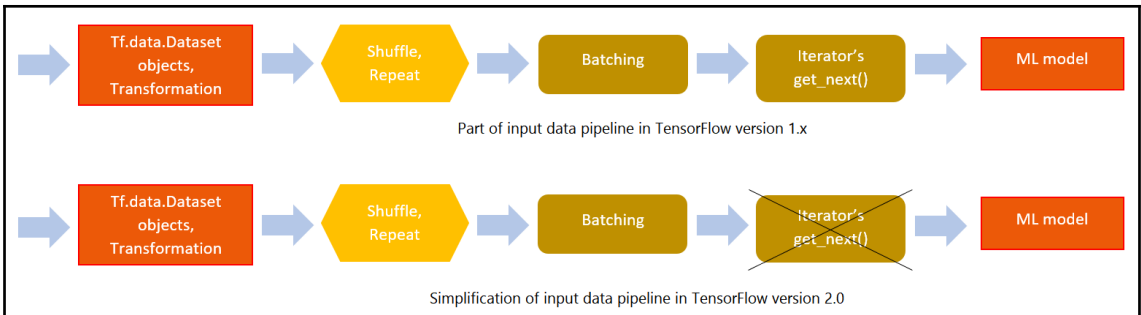
Chapter 3: Designing and Constructing Input Data Pipelines



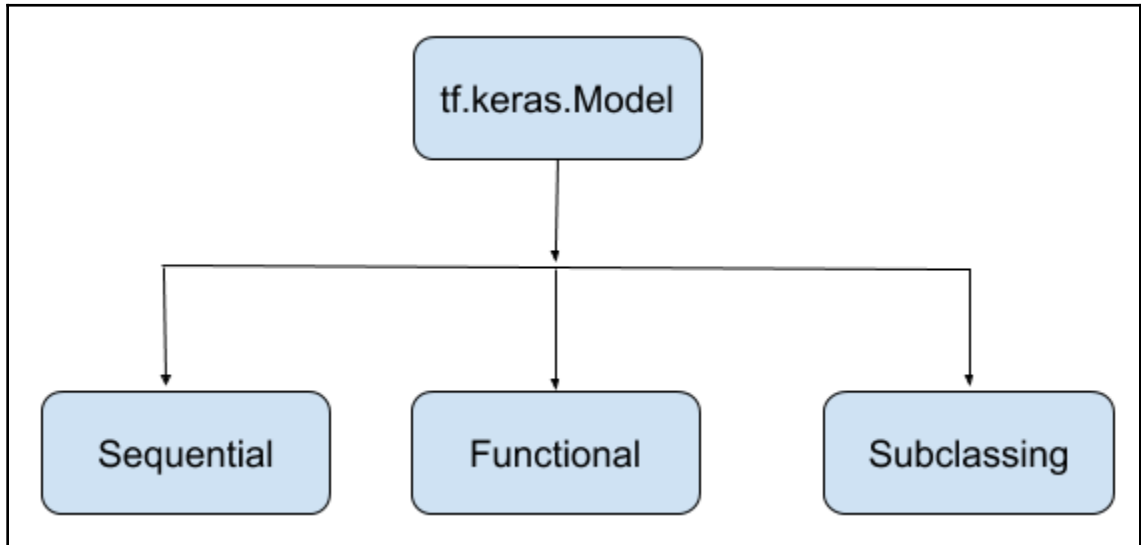
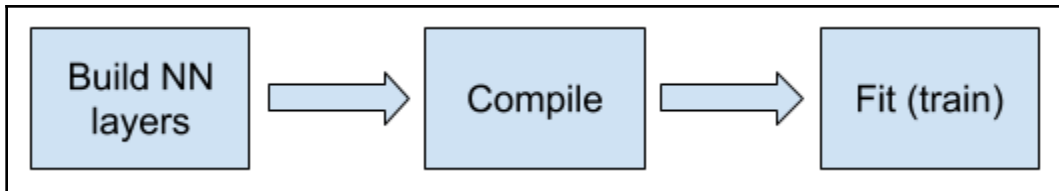
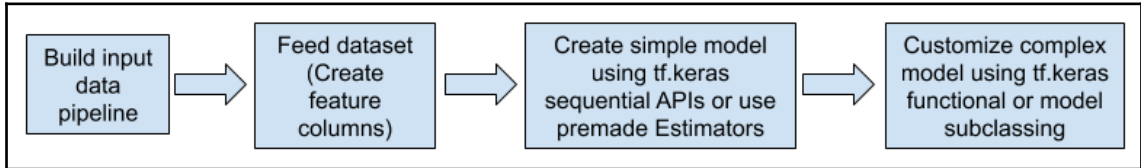


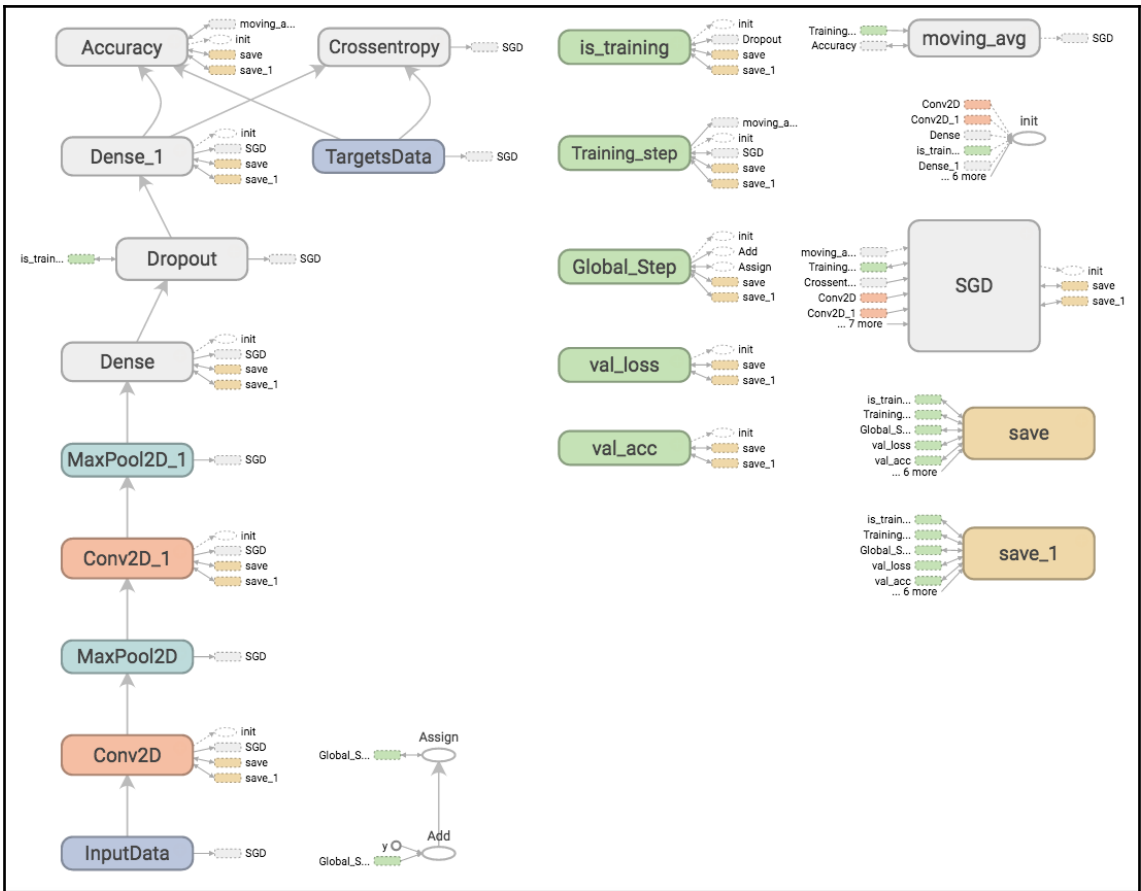
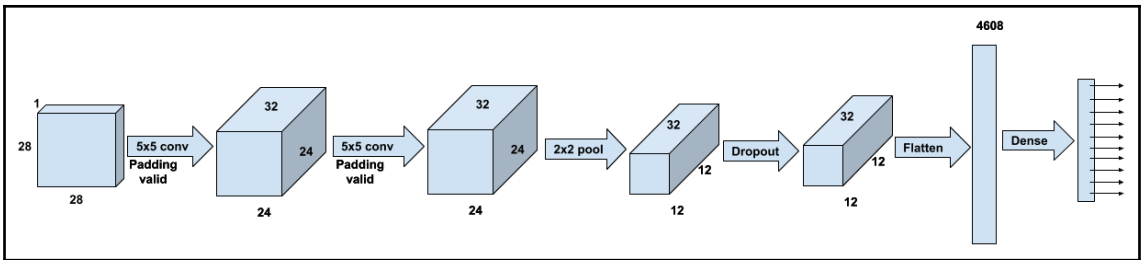







Chapter 4: Model Training and Use of TensorBoard





Search nodes. Regexes supported.

 Fit to Screen

 Download PNG

Run (1) `fit/20190302-183635/train`

Tag (3) `Default`

Upload `batch_2`

`keras`

Conceptual Graph

Profile

Trace inputs

Color Structure

Main Graph



TensorBoard SCALARS HPARAMS INACTIVE

Hyperparameters

- num_units
 - 16,000
 - 32,000
- dropout

Min: -infinity
Max: +infinity

- optimizer
 - adam
 - sgd

Metrics

- Accuracy

Min: -infinity Max: +infinity

Status

- Unknown Success
- Failure Running

Sorting

Sort by:
 Direction:

Paging

Number of matching session

Session Group Name	Show Metrics	num_units	dropout	optimizer	Accuracy
3df0d7cf35bec5a...	<input type="checkbox"/>	32,000	0.20000	sgd	0.77550
3ec2aed9e07589f...	<input type="checkbox"/>	32,000	0.20000	adam	0.82650
53bf5bec9190fa...	<input type="checkbox"/>	16,000	0.20000	adam	0.81540
5b97f3c2967245b...	<input type="checkbox"/>	16,000	0.10000	adam	0.83210
6826c7fa3322d82...	<input type="checkbox"/>	32,000	0.10000	adam	0.83950
7684dcc13358fd0...	<input type="checkbox"/>	16,000	0.20000	sgd	0.76830
7b29a7f31e3daca...	<input type="checkbox"/>	32,000	0.10000	sgd	0.78530
ae235909ec4e4d...	<input type="checkbox"/>	16,000	0.10000	sgd	0.77700

TensorBoard GRAPHS PROJECTOR PROFILE INACTIVE

Runs (5)
2018-06-13_22:30:15/ 1

Tools (5)
trace_viewer 2

Hosts (1)
10.4.108.2 3

Trace View

Timeline: 20 s | 25 s | 30 s

▼ n-1b4d4e4fd-w-0 Chip 2: TPU Core 0 (pid 7)

Step
TensorFlow Ops
XLA Ops

▼ n-1b4d4e4fd-w-0 Chip 2: TPU Core 1 (pid 9)

Step
TensorFlow Ops
XLA Ops

▼ n-1b4d4e4fd-w-0 Chip 3: TPU Core 0 (pid 10)

Step
TensorFlow Ops
XLA Ops

▼ n-1b4d4e4fd-w-0 Chip 3: TPU Core 1 (pid 12)

Step
TensorFlow Ops
XLA Ops

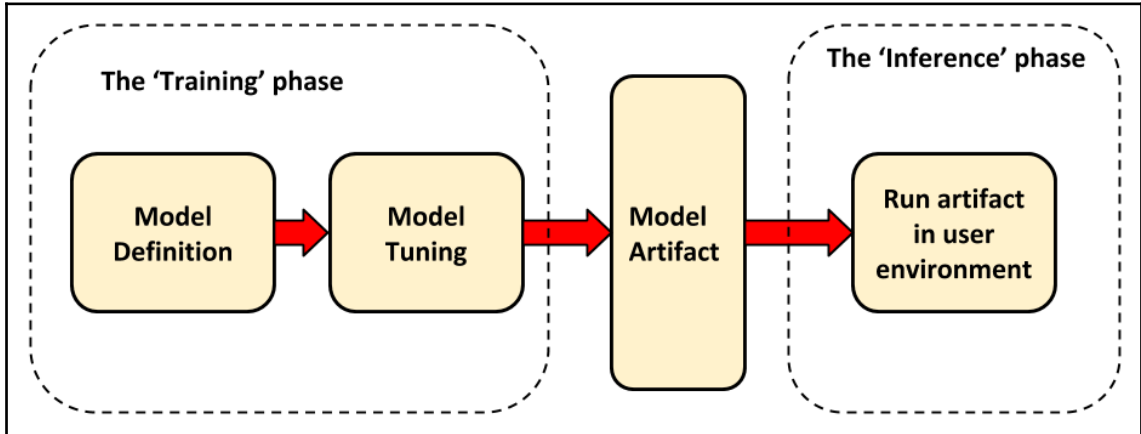
▼ n-1b4d4e4fd-w-0 Host Threads (pid 49)

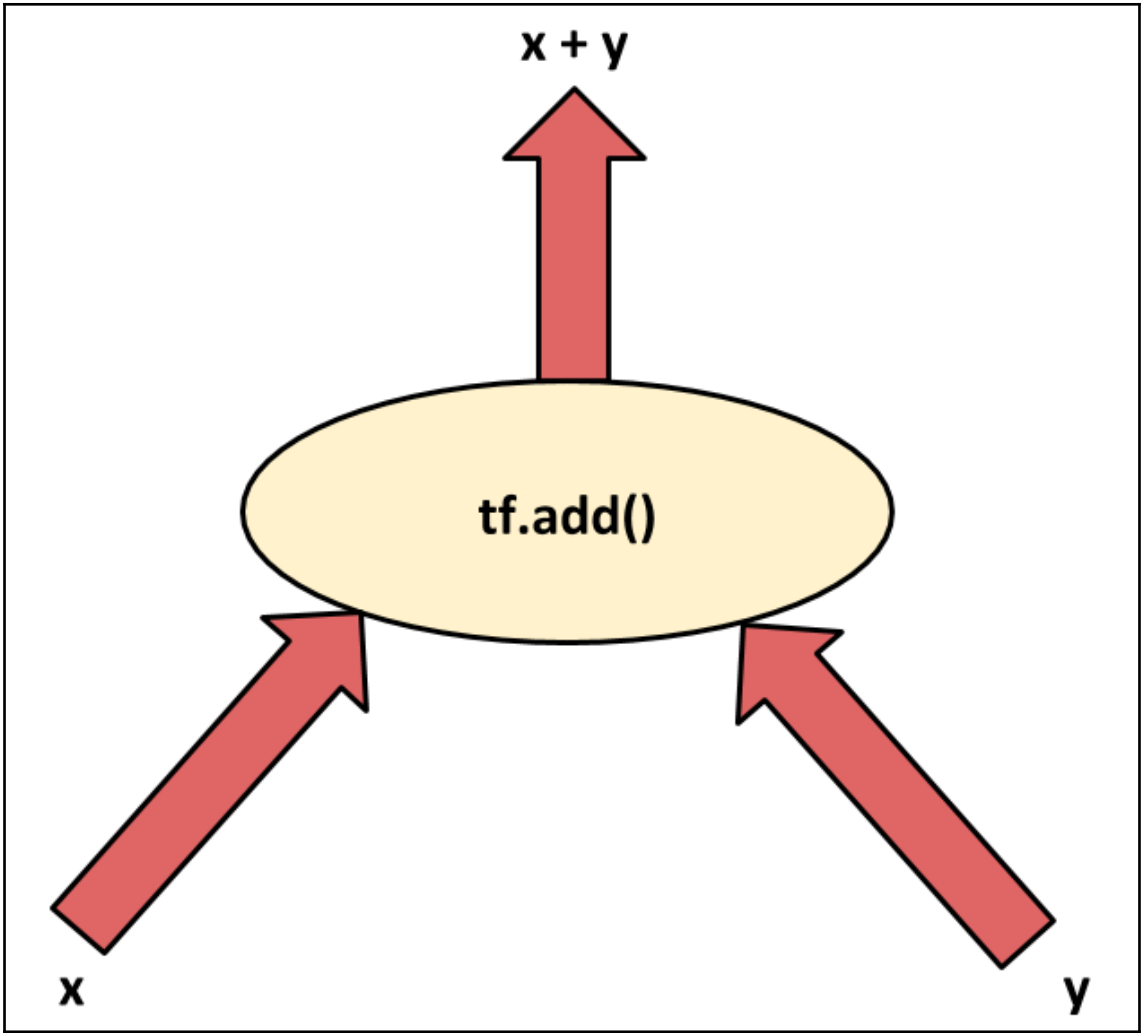
tf_Compute/589
tf_Compute/590
tf_Compute/591
tf_Compute/592

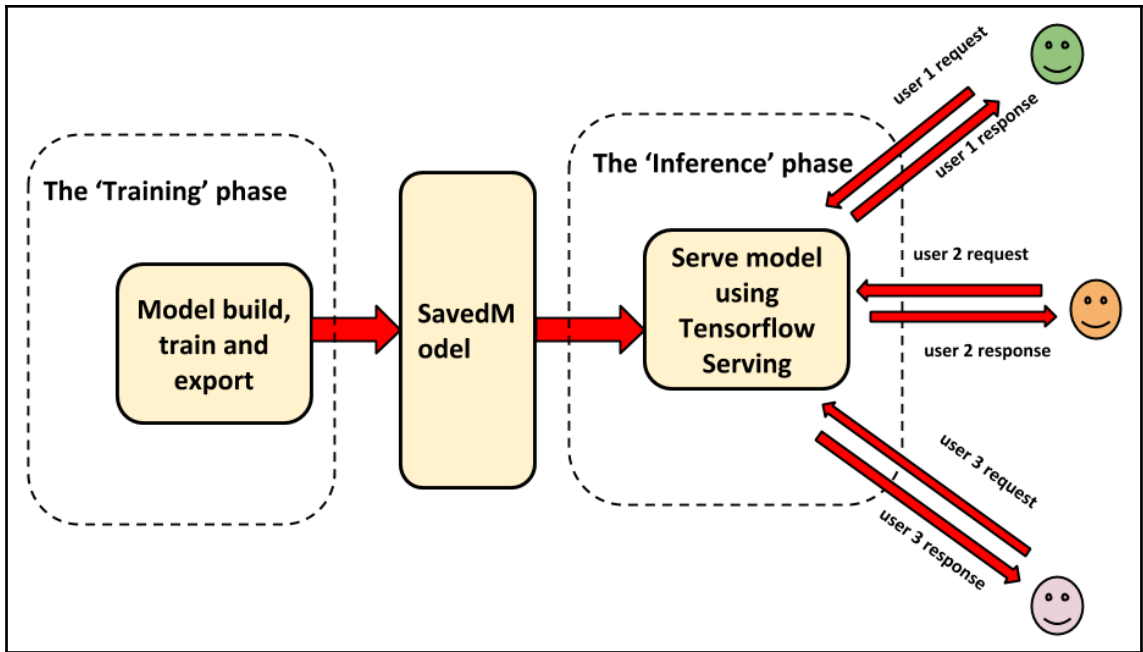
1 item selected. Slice (1)

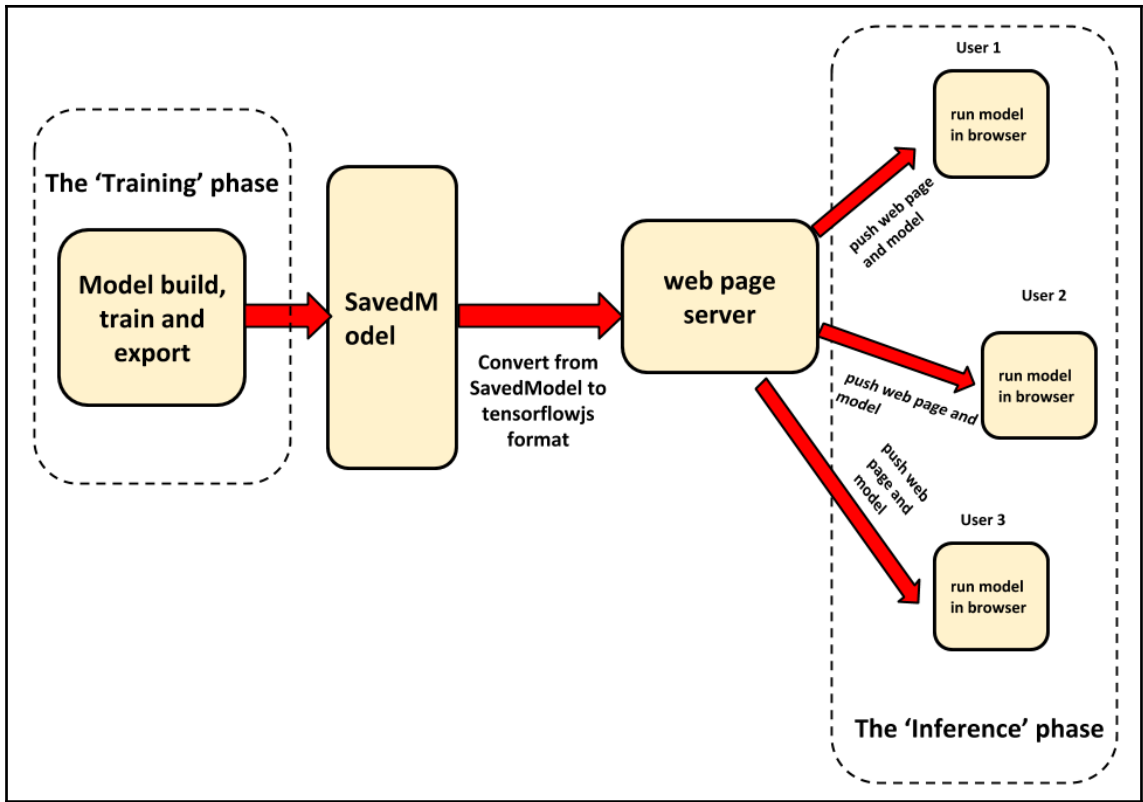
Title	infeed
User Friendly Category	other
Start	21,797,153,135 ns
Wall Duration	1,703,834,979 ns

Chapter 5: Model Inference Pipelines - Multi-platform Deployments









Chapter 6: AIY Projects and TensorFlow Lite

