

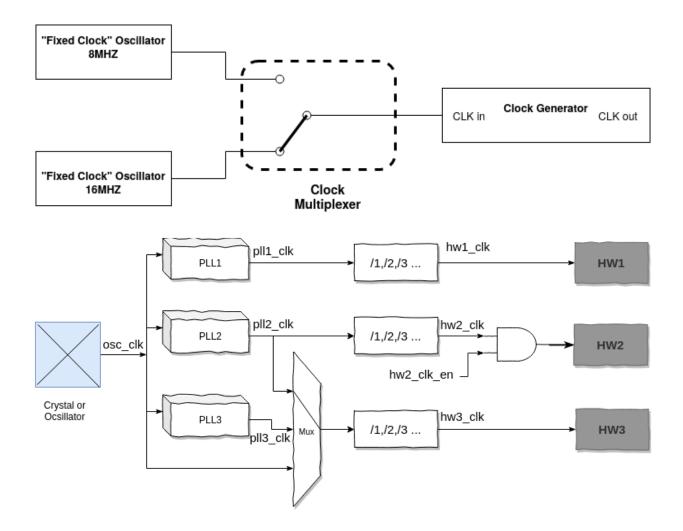
Chapter 1: Linux Kernel Concepts for Embedded Developers

Chapter 2: Leveraging the Regmap API and Simplifying the Code

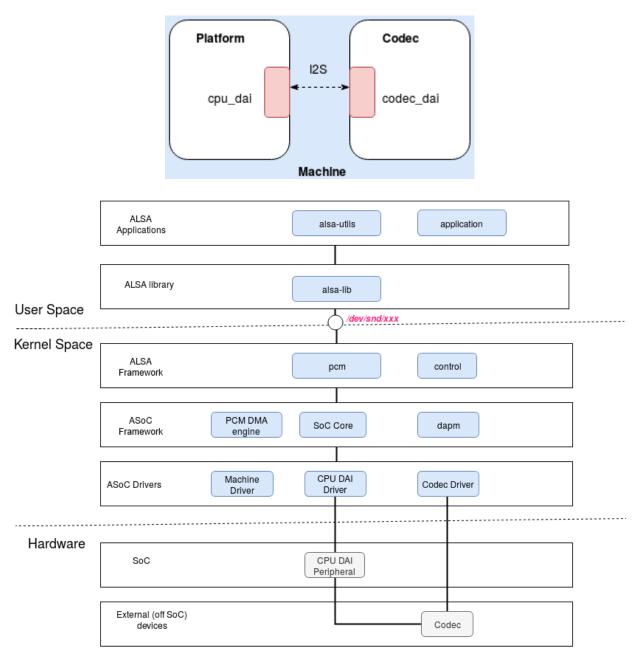
Chapter 3: Delving into the MFD Subsystem and Syscon API

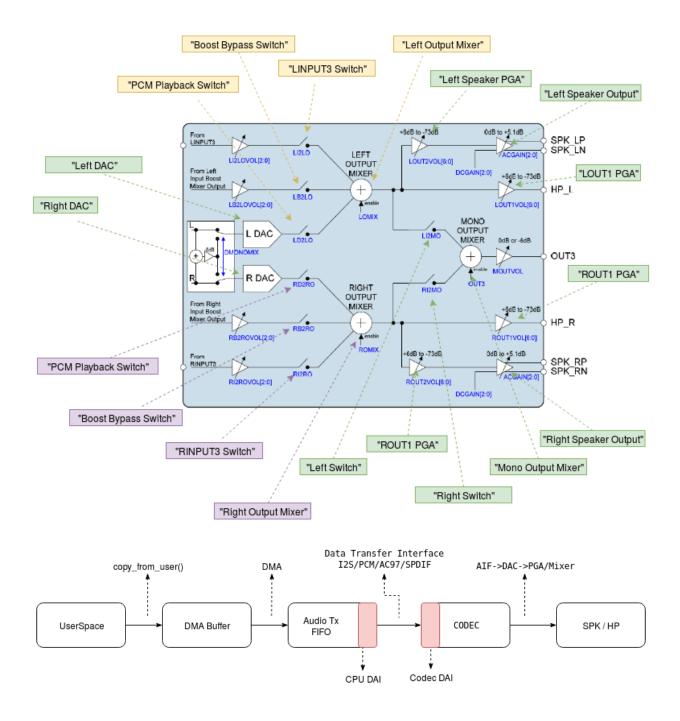
Chapter 4: Storming the Common Clock Framework

+	++		+	+	++
l	gate	change rate	single parent	multiplexer	root
.prepare				I	
.unprepare			, +		
+					
	ly I		 •	 	
.disable	ly I			i i	
.is_enabled +	ly I			l	
.recalc_rate		у		l	
.round_rate		y **	1		
.determine_rate		y **	l		
.set_rate		у	 		
+	++		+		
.set_parent			n	у	n
1-0-1			n		n I
				+	
.recalc_accuracy				I	
+					
.init					
+	++		+	+	



Chapter 5: ALSA SoC Framework - Leveraging Codec and Platform Class Drivers



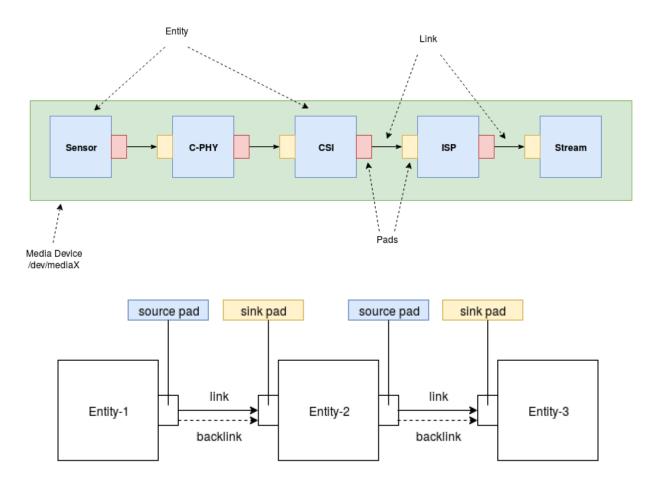


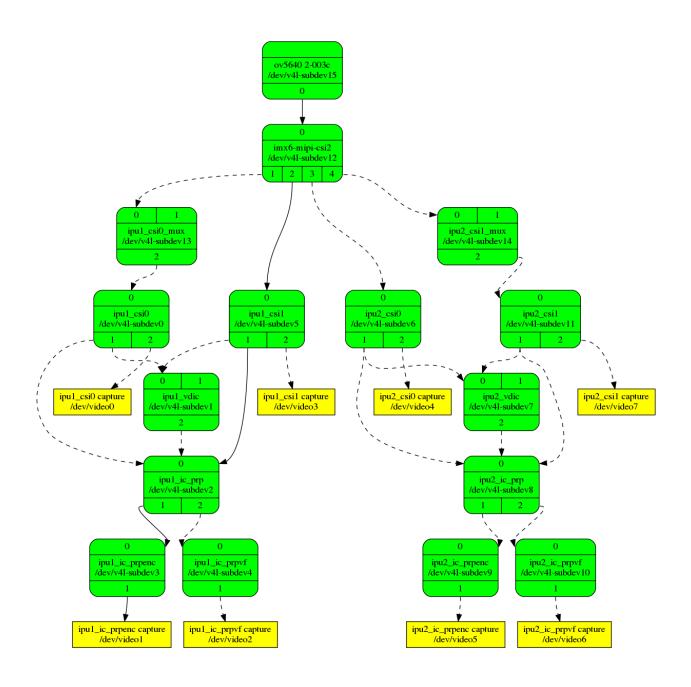
Chapter 6: ALSA SoC Framework - Delving into the Machine Class Drivers

Chapter 7: Demystifying V4L2 and Video Capture Device Drivers

<mark>No Images</mark>

Chapter 8: Integrating with V4L2 Async and Media Controller Frameworks

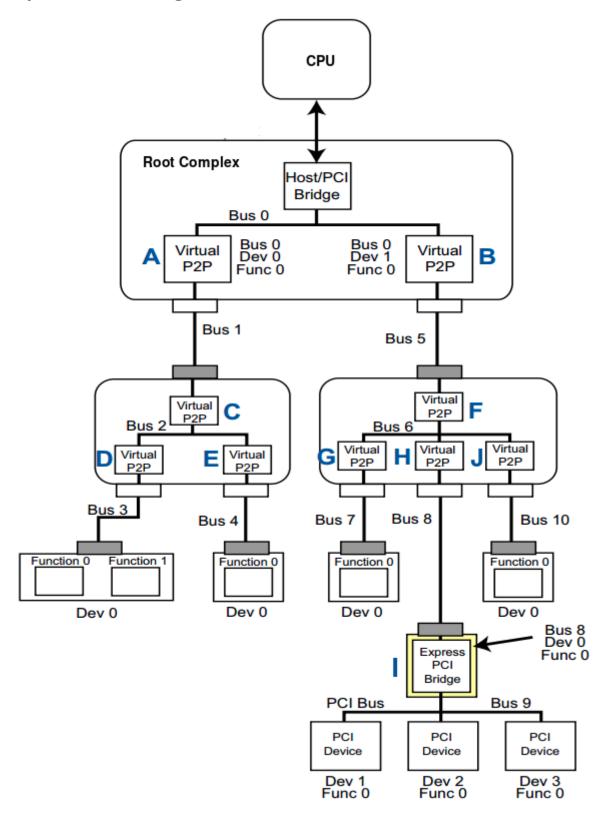




Chapter 9: Leveraging the V4L2 API from the User Space

Chapter 10: Linux Kernel Power Management

Chapter 11: Writing PCI Device Drivers



Chapter 12: Leveraging the NVMEM Framework

Chapter 13: Watchdog Device Drivers

Chapter 14: Linux Kernel Debugging Tips and Best Practices

<pre># entries-in-buffer/en</pre>	tries-written	: 72097/18470	91 #P:1		
#					
#		=> irqs-of	f		
#		=> need-res			
¥					
<pre>#</pre>					
#	<u> /</u>	delay			
# TASK-PID	CPU#	TIMESTAMP	FUNCTION		
#					
mmcqd/0-917	[000] d.h5		irq_may_run <-handle_fasteoi_irq		
mmcqd/0-917	[000] d.h5		handle_irq_event <-handle_fasteoi_irq		
mmcqd/0-917	[000] d.h5		<pre>preempt_count_sub <-handle_irq_event</pre>		
mmcqd/0-917	[000] d.h4		handle_irq_event_percpu <-handle_irq_event		
mmcqd/0-917			dw_mci_interrupt <-handle_irq_event_percpu		
mmcqd/0-917	[000] d.h4		dw_mci_cmd_interrupt <-dw_mci_interrupt		
mmcqd/0-917	[000] d.h4		tasklet_schedule <-dw_mci_cmd_interrupt		
mmcqd/0-917	[000] d.h4		raise_softirq_irqoff <tasklet_schedule< td=""></tasklet_schedule<>		
mmcqd/0-917	[000] d.h4		<pre>add_interrupt_randomness <-handle_irq_event_percpu</pre>		
mmcqd/0-917	[000] d.h4		<pre>read_current_timer <-add_interrupt_randomness</pre>		
mmcqd/0-917	[000] d.h4		<pre>note_interrupt <-handle_irq_event_percpu</pre>		
mmcqd/0-917	[000] d.h4		<pre>preempt_count_add <-handle_irq_event</pre>		
mmcqd/0-917	[000] d.h5	413.431967:	<pre>gic_eoi_irq <-handle_fasteoi_irq</pre>		
mmcqd/0-917	[000] d.h5	413.431967:	<pre>preempt_count_sub <-handle_fasteoi_irq</pre>		
mmcqd/0-917	[000] d.h4	413.431967:	<pre>irq_exit <handle_domain_irq< pre=""></handle_domain_irq<></pre>		
mmcqd/0-917	[000] d.h4	413.431967:	<pre>preempt_count_sub <-irq_exit</pre>		
mmcqd/0-917	[000]s4	413.431967:	tasklet_action <do_softirq< td=""></do_softirq<>		

tracer: nop

entries-in-buffer/entries-written: 35988/35988 #P:8
#=> irqs-off # /=> need-resched
/> hardirg/softirq # /=> preempt-depth
TASK-PID CPU# TIMESTAMP FUNCTION
bash-16561 [002] 639537.102581: hrtimer_init: hrtimer=000000002ba8a2be clockid=CLOCK_MONOTONIC mode=0x9 bash-16561 [002] 639537.102582: hrtimer_init: hrtimer=000000000ded79d7 clockid=CLOCK_MONOTONIC mode=0x9
bash-16561 [002] 639537.102590: hrtimer_init: hrtimer=000000003d041aad clockid=CLOCK_MONOTONIC mode=REL <idle>-0 [004] d.h. 639537.102680: hrtimer_cancel: hrtimer=000000007df5b21a</idle>
[29255.091518] [<c0301780>] (sysrq_handle_crash) from [<c0302128>] (handle_sysrq+0x98/0x134) [29255.099903] [<c0302128>] (handle sysrq) from [<c030259c>] (write sysrq trigger+0x68/0x78)</c030259c></c0302128></c0302128></c0301780>
[29255.108296] [<c030259c>] (write_sysrq_trigger) from [<c0250a40>] (proc_reg_write+0x78/0x8c)</c0250a40></c030259c>
[29255.116691] [<c0250a40>] (proc_reg_write) from [<c01fcc0c>] (_vfs_write+0x48/0xf4)</c01fcc0c></c0250a40>
[29255.124382] [<c01fcc0c>] (vfs_write) from [<c01fd3fc>] (vfs_write+0xbc/0x144) [29255.131724] [<c01fd3fc>] (vfs write) from [<c01fdbdc>] (SyS write+0x68/0xc0)</c01fdbdc></c01fd3fc></c01fd3fc></c01fcc0c>
[29255.138811] [<c01fdbdc>] (SyS_write) from [<c0107780>] (ret_fast_syscall+0x0/0x1c)</c0107780></c01fdbdc>